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Method and interface for glitch-free clock switching PTO-2040 12/89																
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DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.		
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subsequent to(date) has been disclaimed.	(Assistant	Examiner)	(Date)				
The term of this patent shall not extend beyond the expiration date of U.S Patent. No.				ISS	UE FEE		
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